

## DMD Discovery™ Product Selection Guide

### DMD Discovery™ 1100 for research, development, and evaluation

Components	Chip set	Starter Kit and Board	ALP-1	ALP-2	Remarks
Chip set type	DMD Discovery™ 1100				
DMD type	0.7" XGA 12° DDR				400 – 700 nm
Full array switching rate	9,600 frames/s				phased reset
UV enhanced DMD	available				> 350 nm
NIR enhanced DMD	available				< 2.500 nm
Boards		D1100 board, flexible board Q1/ 2007	Parallel interface board for D1100	Parallel interface board for D1100	ALP-1 and ALP-2 use different boards
PC interface		USB 2.0	USB 2.0	USB 2.0	
FPGA		-	Virtex 2	Virtex 4	no customer access to FPGA
Developer software (MS WIN)		GUI, ActiveX controls	API (C/C++ DLL)	API (C/C++ DLL)	
max transfer rate from PC		~ 100 binary frames/s	≥ 200 binary frames/s	≥ 200 binary frames/s	
on-board memory		-	2 Gbit	32 / 48 / 128 Gbit	ALP-2 with X-RAM daughter board
max frame rate		100 frames/s via USB	8,000 frames/s from on-board memory	8,000 frames/s from on-board memory	global reset
Hardware trigger		-	master or slave mode	master or slave mode	trigger delay and pre-trigger supported
Gray value patterns		-	1-8 bit PWM	1-8 bit PWM	

### DMD Discovery™ 3000 for the production of products

Components	Chip set	Starter Kit and Board	ALP-3 basic	ALP-3 high-speed	Remarks
Chip set	DMD Discovery™ 3000				
DMD type	0.7" XGA 12° LVDS 0.9 SXGA+ 12° LVDS				400-700 nm
Full array switching rate	16,300 frames/s				phased reset
UV enhanced DMD					not available
NIR enhanced DMD	-				not available
Boards		D3000 board for XGA, optional: flexible cable extension	Daughter board for D3000	Daughter board for D3000	ALP-3 models use the same daughter board
PC interface		None	USB 2.0	USB 2.0	
FPGA		Virtex 4	uses Virtex 4 on D3000	uses Virtex 4 on D3000	
Developer software (MS WIN)		-	GUI, API (DLL)	API (DLL)	
max transfer rate from PC		-	~ 10 binary frames/s	≥ 200 binary frames/s	
on-board memory		1 Gbit	not used	1 Gbit on D3000 board	
max frame rate		-	~30 frames/s via USB	13,333 frames/s from on-board memory	global reset
Hardware trigger		-	-	master or slave mode	trigger facilities supported
Gray value patterns		-	-	1-8 bit PWM	